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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Eric NEYRET et al.

Confirmation No. 6832

Application No.: 10/671,813

Group Art Unit: 2814

Filing Date: September 25, 2003

Examiner: Dilinh P. Nguyen

For: METHOD FOR MINIMIZING SLIP LINE  
FAULTS ON A SEMICONDUCTOR  
WAFER SURFACE

Attorney Docket No.: 4717-7800

**AMENDMENT AFTER FINAL**

**Mail Stop AF**

Commissioner for Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

Sir:

In response to the final office action dated November 30, 2005, please enter the following amendments and comments into the file of this application.

**Amendments to the Specification** appear herein starting on page 2.

**Amendments to the Claims** are reflected in the complete listing of all claims which begin on page 3 of this paper.

**Remarks** begin on page 7 of this paper.

No fee is believed to be due for this response. Please charge any required fees to Winston Strawn LLP Deposit Account No. 50-1814.